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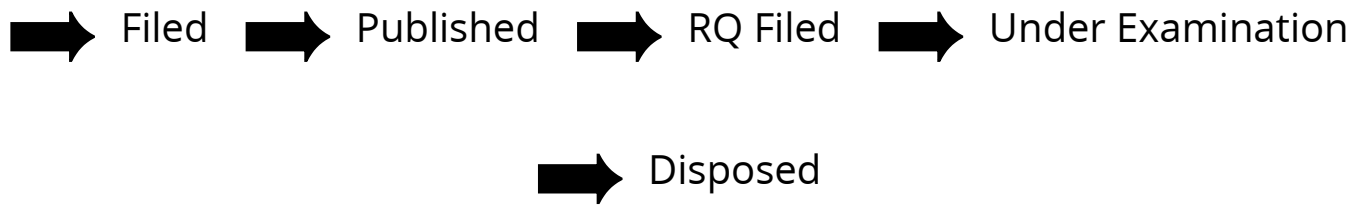
Application Details	
APPLICATION NUMBER	202011033495
APPLICATION TYPE	ORDINARY APPLICATION
DATE OF FILING	05/08/2020
APPLICANT NAME	1 . DR. SHUBHAM TAYAL 2 . DR. ANJU BHANDARI GANDHI 3 . DR. ASHUTOSH NANDI 4 . DR. SUNEET KUMAR 5 . DR. PRACHI GARG 6 . ANSHUL RANI 7 . SHIVANI 8 . DEEPAK KUMAR 9 . DR. HEMACHANDRANIK 10 . DR. SHARAD SHARMA 11 . ANURAG
TITLE OF INVENTION	LOW-K SIDE-WALL SPACER BASED INNER GATE ENGINEERED JUNCTIONLESS SILICON NANOTUBE FET
FIELD OF INVENTION	CHEMICAL
E-MAIL (As Per Record)	
ADDITIONAL-EMAIL (As Per Record)	swati.elle1990@gmail.com
E-MAIL (UPDATED Online)	
PRIORITY DATE	
REQUEST FOR EXAMINATION DATE	--
PUBLICATION DATE (U/S 11A)	04/09/2020

Application Status

APPLICATION STATUS

## Awaiting Request for Examination

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FORM  
THE PATENT ACT, 1970  
( 39 OF 1970 )  
&  
The Patents Rules, 2003  
DECLARATION AS TO INVENTORSHIP  
[See section 10(6) and rule 13(6)]

1. NAME OF APPLICANT (S)

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10. Dr. Sharad Sharma
11. Arun Rana

hereby declare that the true and first inventor(s) of the invention disclosed in the provisional specification filed in pursuance of my / our application numbered ..... filed on ddmm/yyyy are

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Dated this 30<sup>th</sup> day of June 2020

3. DECLARATION TO BE GIVEN WHEN THE APPLICATION IN INDIA IS FILED BY THE APPLICANT (S) IN THE CONVENTION COUNTRY:- NA

Dated this 30<sup>th</sup> day of June 2020

Signature :-

To,  
The Controller of Patent  
The Patent Office, at Delhi